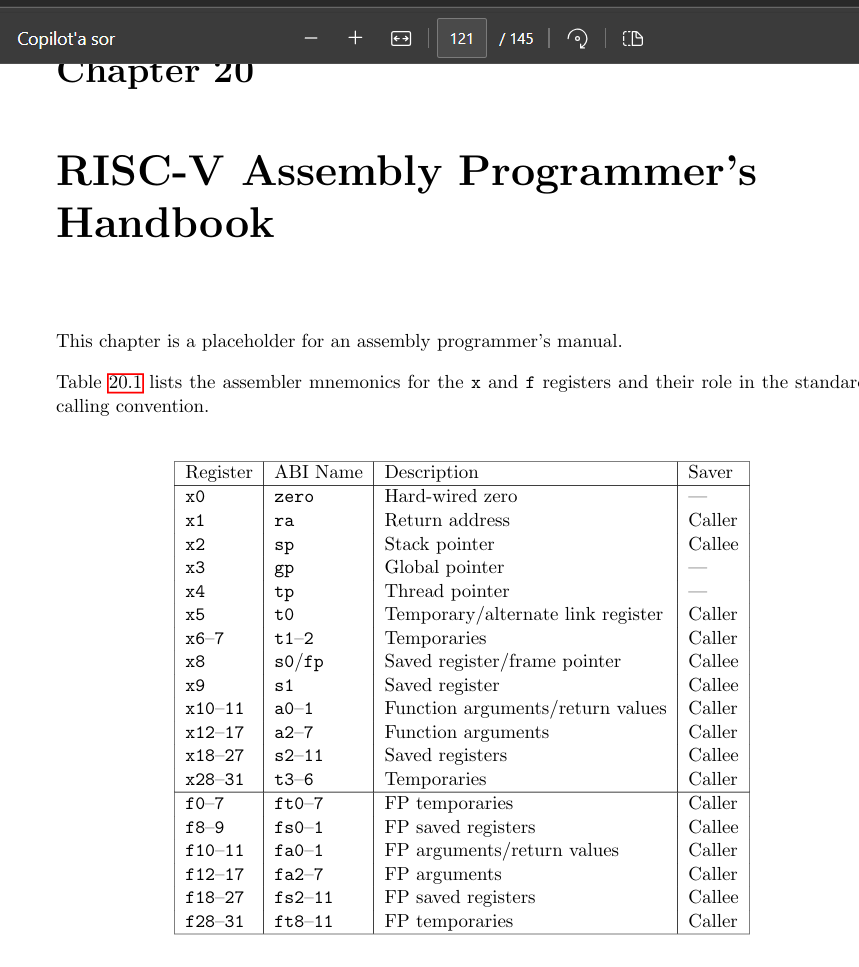
Register



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Description automatically generated with medium confidenceInstruction assembler pseudoinstruction

RV32I Base Instruction set

A document with numbers and letters

Description automatically generated

Refecrences

<https://gse.ufsc.br/bezerra/wp-content/uploads/2022/04/Lecture7.pdf>

<https://sites.google.com/site/embeddedmonologue/home/c-programming/logic-and-arithmetic-shift-and-divide-by-2>

<https://projectf.io/posts/riscv-branch-set/#branch>

<https://pages.hmc.edu/harris/ddca/ddcarv/ddcarv_videos.html>

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<https://lf-riscv.atlassian.net/wiki/spaces/HOME/pages/16154769/RISC-V+Technical+Specifications>